



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
 United States Patent and Trademark Office
 Address: COMMISSIONER FOR PATENTS
 P.O. Box 1450
 Alexandria, Virginia 22313-1450
 www.uspto.gov



Bib Data Sheet

CONFIRMATION NO. 6665

SERIAL NUMBER 10/650,296	FILING DATE 08/27/2003 RULE	CLASS 716	GROUP ART UNIT 2825	ATTORNEY DOCKET NO. 03-0861 81580
-----------------------------	---------------------------------------	--------------	------------------------	-----------------------------------------

APPLICANTS

Alexander Tetelbaum, Hayward, CA;

** CONTINUING DATA *N* *VS*** FOREIGN APPLICATIONS *N* *VS*IF REQUIRED, FOREIGN FILING LICENSE
GRANTED

** 11/18/2003

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY CA	SHEETS DRAWING 9	TOTAL CLAIMS 18	INDEPENDENT CLAIMS 2
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged	Examiner's Signature <i>Vincent S. [Signature]</i>	Initials <i>VS</i>		

ADDRESS

LSI Logic Corporation
 Corporate Legal Department
 Intellectual Property Services Group
 1551 McCarthy Blvd., M/S D-106
 Milpitas, CA
 95035

TITLE

Method of clock driven cell placement and clock tree synthesis for integrated circuit design

FILING FEE RECEIVED 750	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees
		<input type="checkbox"/> 1.16 Fees (Filing)
		<input type="checkbox"/> 1.17 Fees (Processing Ext. of time)
		<input type="checkbox"/> 1.18 Fees (Issue)
		<input type="checkbox"/> Other _____
		<input type="checkbox"/> Credit